WEST Search History

| Hide Items | Restore | Clear | Cancel |
|------------|---------|-------------|--------|
| | | 1 1000 1000 | |

DATE: Saturday, March 04, 2006

| Hide? | <u>Set</u> Name | Query | <u>Hit</u> Count |
|-------|--------------------|---|---------------------|
| | DB=PC | GPB; PLUR=YES; OP=ADJ | |
| | L2 | (prefetch circuit to prefetch a data block into the memory in advance of a subsequent reads).clm. | 0 |
| | L1 | (prefetch circuit to prefetch a data block into the memory in advance of a subsequent read).clm. | 0 |

END OF SEARCH HISTORY